



Republic of the Philippines
Office of the President
Philippine Space Agency



Project Technical Specialist IV

Status: Contract of Service

Monthly Salary: Php 61,628.00 (*inclusive of 20% premium*)

Item Code: COS-PTS4-134

Division: Spacecraft Payload and Communications Systems Development Division

Required Qualification:

Education:	Bachelor's Degree in engineering and sciences such as Aerospace Engineering, Electronics Engineering, Computer Engineering, Computer Science or any other allied fields
Experience:	2 years of relevant experience in Embedded systems firmware/software development, prototyping, and development
Training:	None required
Eligibility:	None required

Duties and Responsibilities:

- 1) Serve as the Subsystems Lead for embedded software/firmware development for the spaceborne spectrum monitoring (DEEPWISDOM) payload;
- 2) Study the Interface Control Document (ICD) and other related technical documentation for the Faraday Dragon satellite rideshare mission;
- 3) Study the reference firmware/software design workflow document for the OD-SHARES SDR module from the ASPI3RE project;
- 4) Contribute to the formulation of the mission definition, system requirements, and payload preliminary design for the DEEPWISDOM payload;
- 5) Develop and test the embedded firmware components for implementing the desired functionalities of the embedded SoCs for the SDR payload using Vivado or other suitable software;
- 6) Develop and test the embedded software components required to implement the functionalities of the SDR module using Xilinx SDK or other suitable software;
- 7) Plan and perform functionality testing of the embedded firmware/software components for the SDR module;
- 8) Assist in preparing all required technical documentation related to the development of the SDR module;
- 9) Work closely with the Project Leader, Technical Lead, and other project members regarding the plans, requirements, concerns, problems, and other project-related matters.
- 10) Coordinate and work closely with internal and external technical peer and QA reviewers regarding the development of the SDR firmware and software components.
- 11) Provide regular reports or updates on the progress of the assigned tasks;
- 12) Submit pertinent documentation/s the accomplished tasks at the end of the contract;
- 13) Must agree that all information shared to him/her is confidential and cannot be used for any other purpose, except those that are publicly available;
- 14) Perform other duties of a regular or special nature as may be assigned from time to time.

Applicants who have all or any of the following background are encouraged to apply:

Experience/Training Background:

- Embedded systems firmware/software development using Vivado and Xilinx SDK
- Experience in embedded systems prototyping and development using Xilinx FPGAs and System-on-Chips
- Experience in design verification and software/firmware design reviews (peer and QA review) is a plus
- Experience/training in satellite systems engineering and/or satellite/payload development projects is a plus

Competencies:

- Must be proactive. Takes prompt action to accomplish tasks and meet goals and objectives in a timely manner.
- Must be thorough. Pays close attention to detail and strives to achieve accuracy and consistency in all tasks.
- Develops and maintains positive work relationships with others.
- Willingness to learn and acquire diverse skills over different fields.
- Proficient in developing firmware/software for Xilinx FPGA/SoCs, either via Matlab/Simulink or manual HDL coding.
- Skills in simulating and debugging embedded firmware/software issues
- Familiarity or proficiency in SDR-related module/payload development is a plus



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